

F I G. 2

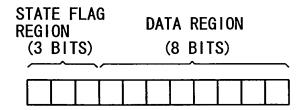
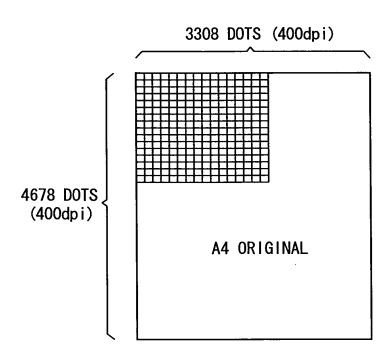


FIG. 3

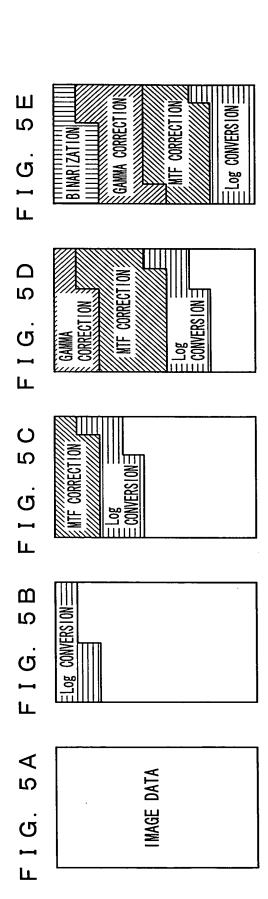
PROCESSING	PROCESSIBLE FLAG	PROCESSED FLAG
Log CONVERSION	0 0 0	0 0 1
MTF CORRECTION	0 0 1	0 1 0
GAMMA CORRECTION	0 1 0	0 1 1
BINARIZATION	0 1 1	1 1 0
OUTPUT	1 1 0	1 1 1

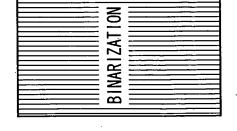
APPROVED	O.G. FIG.	
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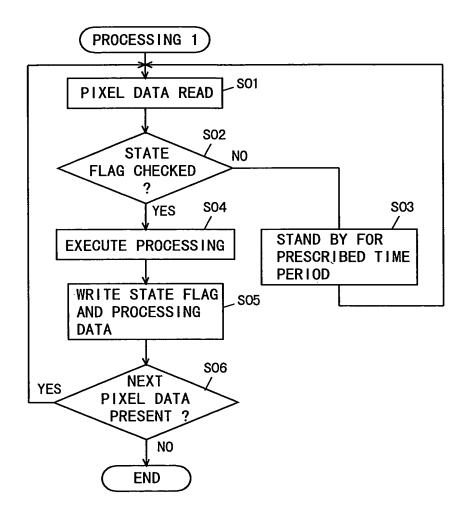
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Я П. С. APPROVED O.G. FIG.

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FIG. 6



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FIG. 7A

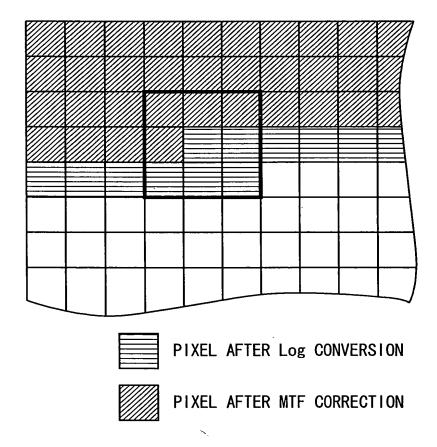
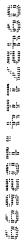


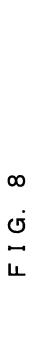
FIG. 7B

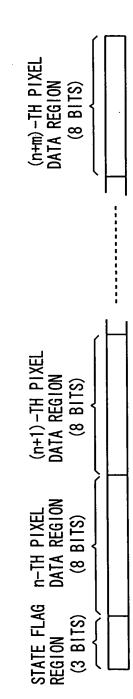
PIXEL FOR MTF CORRECTION

3×3 MATRIX

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2 DATA BUS IMAGE OUTPUT DEVICE BINARIZATION MEMORY GAMMA CORRECTION MTF CORRECTION Log CONVERSION 5 ∞ 2 STATE CONTROL PORT I ON IMAGE INPUT Device CONTROL SIGNAL

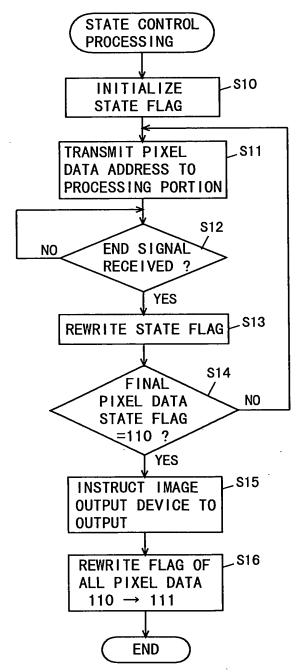
F I G. 9

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B B Hard Bloom

FIG. 10

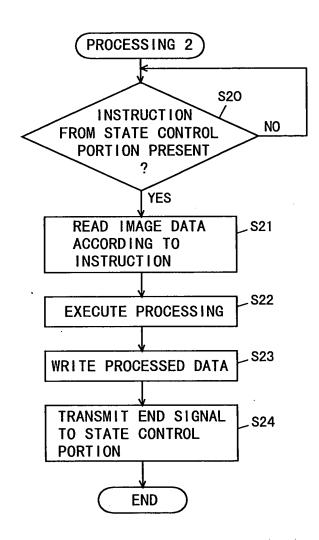


SUBCLASS

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FIG. 11



BINARIZATION GAMMA CORRECTION 9 CORRECT I ON REGION DETERMINING PORTION Log CONVERSION Φ. STATE CONTROL PORTION CONTROL SIGNAL IMAGE INPUT Device

IMAGE OUTPUT DEVICE

DATA BUS

MEMORY

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FIG. 12

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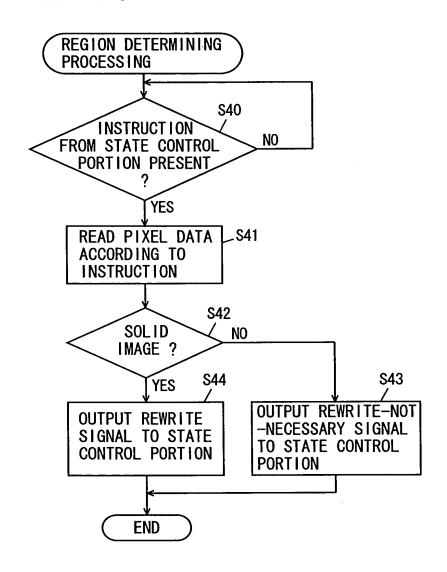
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O.G. FIG.
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FIG. 13



APPROVED O.G. FIG.
BY CLASS SU SUBCLASS DRAFTSMAN

FIG. 14

PROCESSING	PROCESSIBLE FLAG	PROCESSED FLAG
REGION	0 0 0	0 0 1
DETERMINING		1 0 0
Log CONVERSION	0 0 1	0 1 0
MTF CORRECTION	010	0 1 1
GAMMA CORRECTION	0 1 1	1 0 0
BINARIZATION	1 0 0	1 1 0
OUTPUT	1 1 0	1 1 1

DATA BUS IMAGE OUTPUT DEVICE 47 MEMORY GAMMA CORRECTION MTF CORRECTION REGION DETERMINING PORTION 46 COLOR CONVERSION STATE CONTROL PORTION 45 IMAGE Input Device CONTROL SIGNAL

FIG. 15





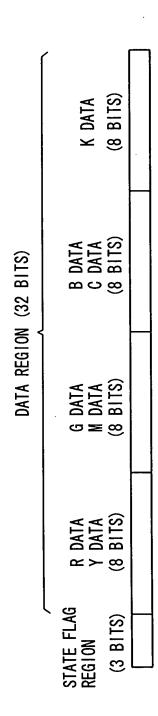


FIG. 17

PROCESSING	PROCESSIBLE FLAG	PROCESSED FLAG
REGION DETERMINING	0 0 0	0 0 1
		1 0 1
COLOR CONVERSION	0 0 1	0 1 0
	1 0 1	1 1 0
MTF CORRECTION	0 1 0	0 1 1
GAMMA CORRECTION	0 1 1	1 1 0
OUTPUT	1 1 0	111

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